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JC525 U.S. PTO
09/512149



1 Inventor: Vishnu K. Agarwal

2 Title: Integrated Circuitry And Method Of Forming A Capacitor

3 Assignee: Micron Technology, Inc.

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5 INFORMATION DISCLOSURE STATEMENT

6 References -- See Attached Form PTO-1449

7 The attached form PTO-1449 is submitted in compliance with
8 37 CFR §1.56. No admission is made regarding whether all the
9 submitted references are prior art.

10 Respectfully submitted,

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12 Dated: 14th February 2000

Inventor:

Vishnu K. Agarwal

13
14 Dated: 2-23-00

Attorney:

Mark S. Matkin
Reg. No.: 32,268